

**ABSTRACT**

A power amplifier circuit including first and second cascade-assembled operational amplifiers having respective first inputs receiving a reference voltage across a decoupling capacitor, respective outputs of which are connected across a load and are 5 looped back on respective second inputs, the second input of the first amplifier receiving, from a coupling capacitor, an input voltage to be amplified, the amplifier circuit including means for separately charging the coupling and decoupling capacitors, upon circuit power-on, from an off or standby state, and means for inhibiting the amplifiers at least during the separate charging.